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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/706,238

11/12/2003

Makiko Kan

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6389

26263

7590

12/21/2005

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EXAMINER

LIN, SUN J

ART UNIT

PAPER NUMBER

2825

DATE MAILED: 12/21/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/706,238

Applicant(s)

KAN, MAKIKO

Examiner

Sun J. Lin

Art Unit

2825

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 30 November 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-10 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 3-8 is/are allowed.
- 6) ☒ Claim(s) 1,2,9 and 10 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 12 November 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

1. This Office Action is in response to applicant's Amendment and Remarks filed on 10/30/2005 regarding application 10/706,238 filed on 11/12/2003. Claims 1 – 10 remain pending in the application.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claim 1, 2, 9 and 10 are rejected under 35 U.S.C. 102(b) as being unpatentable over U.S. Patent No. 4,967,340 to Dawes.

4. As to Claim 1, Dawes shows and teaches the following subject matter:

- A method in design of a processing circuit for performing linear (transformation) operation on predetermined data – [col. 3, line 51 – 54]; Linear data processing operations (i.e., linear transform processings), , which is a **matrix multiplication**, typically involves “multiplying-and-accumulate (i.e., addition)” type of expressions ... processing (circuit) elements – multiplication element (multiplier) and addition element (adder) – [col. 3, lines 51 – 61]; linear transformation ... vector inner product ... matrix multiplication – [col. 1, lines 48 – 52]; Notice that (1) a linear transform processing involves vector inner product (i.e., a first processing) and addition processing (i.e., a second processings) (2) each linear transform processing requires a different number of addition processings , which is dependent upon on the nature (e.g., rank) of the matrix, to be included in the (vector) inner product processing (3) due to hardwired configuration, required number of **addition processings** (second processings) to be included in the (vector) inner product processing of a linear transform processing should be **identified/defined** before designing the first processing (linear transform

processing) – [col. 4, lines 21 – 38]; It is inherent that, to save design effort and share common device resources, addition processings performing the same number of additions on the same data in pluralities of linear transformation processings should be identified;

- Parallel signal processing techniques ... adaptive signal/data processing operation – [col. 1, lines 11 – 14]; **processing modules** – modularity of (data) processing elements/cells to facilitate **economic implementation** of data processing in parallel fashion and to achieve **high computational throughput** – [col. 1, lines 28 – 38]; Notice that, due to the modularity of (data) processing modules, inner product processing module for use in performing linear transformation is adaptively being shared and utilized by different first processing circuits and performing the identified number of addition processings, which is common to the different first processing circuits;
- Signal/data processing involves repetitive calculation (e.g., linear transform processing) – [col. 1, line 17 – 20];

In addition, Dawes discloses the following subject matter:

- First processing elements (i.e., first processing circuit), which perform linear operations (i.e., linear processing) on an input data – [col. 4, line 21 – 38];
- Non-linear operations on an input data, which is done by transferring data to a central processor (i.e., second processing circuit) for nonlinear calculation – [col. 4, line 35 – 38].

For reference purposes, the explanations given above in response to Claim 1 are called **[Response A]** hereinafter.

5. As to Claims 9 and 10, reasons are the same as recited in **[Response A]** given above.

6. As to Claim 2, reasons are included in **[Response A]** given above.

Allowable Subject Matter

7. Claim 3 – 8 are allowed. These claims are allowed because Claim 3, which was a dependent claim and was allowed due to allowable subject matter as explained in the Office Action mailed 09/09/2005, is rewritten in an independent form including all of the limitations of the base claim and any intervening claims.

Response to Amendment and Remarks

8. Applicant's amendments and remarks filed on 11/30/2005 have been reviewed. Applicant's arguments have been fully considered. Office Action is modified in response to applicant's argument regarding inherency.

Conclusion

9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Sun James Lin whose telephone number is (571) 272 - 1899. The examiner can normally be reached on Monday-Friday 9:30AM - 6:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jack Chiang can be reached on (571) 272 - 7483. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Sun James Lin
Patent Examiner
Art Unit 2825
December 20, 2005

